

Electronic Acknowledgement Receipt

EFS ID:	1229003
Application Number:	09915437
Confirmation Number:	7370
Title of Invention:	Method of logic circuit synthesis and design using a dynamic circuit library
First Named Inventor:	Sang Hoo Dhong
Customer Number:	35236
Filer:	Russell Duane Culbertson/Gracie Solis
Filer Authorized By:	Russell Duane Culbertson
Attorney Docket Number:	AUS9-2001-0301US1
Receipt Date:	29-SEP-2006
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Application Type:	Utility
International Application Number:	

Payment information:

Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part	Pages
1		21_1061_PreAppealBriefRequestandAppealNotice_asfiled.pdf	342002	yes	7

	Multipart Description		
	Doc Desc	Start	End
	Transmittal letter	1	1
	Notice of Appeal Filed	2	2
	Pre-Brief Conference request	3	7

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	8168	no	2
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Warnings:

Information:

Total Files Size (in bytes):	350170
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.